

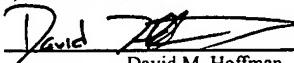
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:
Bill Baggenstoss
Serial No.: 10/609,089
Filed: June 27, 2003
For: CAPACITOR LAYOUT ORIENTATION
§ Group Art Unit: 2824
§ Examiner: Phung, Anh K.
§ Atty. Docket: MICS:0098/FLE/BLT/HOF
§ 02-1041
§

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF TRANSMISSION OR MAILING 37 C.F.R. 1.8	
I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office in accordance with 37 C.F.R. 1.6(d) or is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date below:	
December 16, 2004	Date
 David M. Hoffman	

Dear Sir:

RESPONSE TO REQUIREMENT FOR RESTRICTION

In response to the Requirement for Restriction mailed on November 17, 2004, please amend the above-identified application as follows.

IN THE CLAIMS

Please cancel claims 37-54, without prejudice. Please correct a misnumbering regarding claims 27-37, as currently numbered in this Response.

1. (Original) A plurality of capacitive memory elements disposed on a substrate so that an axis that runs longitudinally through one of the plurality of capacitive memory elements is not generally parallel with an edge of the substrate.